










# Технические характеристики






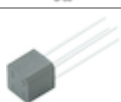
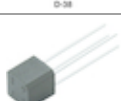
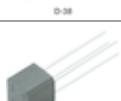
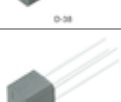

## По вопросам продаж и поддержки обращайтесь:










Алматы (7273)495-231	Казань (843)206-01-48	Новокузнецк (3843)20-46-81	Смоленск (4812)29-41-54
Архангельск (8182)63-90-72	Калининград (4012)72-03-81	Новосибирск (383)227-86-73	Сочи (862)225-72-31
Астрахань (8512)99-46-04	Калуга (4842)92-23-67	Омск (3812)21-46-40	Ставрополь (8652)20-65-13
Барнаул (3852)73-04-60	Кемерово (3842)65-04-62	Орел (4862)44-53-42	Сургут (3462)77-98-35
Белгород (4722)40-23-64	Киров (8332)68-02-04	Оренбург (3532)37-68-04	Тверь (4822)63-31-35
Брянск (4832)59-03-52	Краснодар (861)203-40-90	Пенза (8412)22-31-16	Томск (3822)98-41-53
Владивосток (423)249-28-31	Красноярск (391)204-63-61	Пермь (342)205-81-47	Тула (4872)74-02-29
Волгоград (844)278-03-48	Курск (4712)77-13-04	Ростов-на-Дону (863)308-18-15	Тюмень (3452)66-21-18
Вологда (8172)26-41-59	Липецк (4742)52-20-81	Рязань (4912)46-61-64	Ульяновск (8422)24-23-59
Воронеж (473)204-51-73	Магнитогорск (3519)55-03-13	Самара (846)206-03-16	Уфа (347)229-48-12
Екатеринбург (343)384-55-89	Москва (495)268-04-70	Санкт-Петербург (812)309-46-40	Хабаровск (4212)92-98-04
Иваново (4932)77-34-06	Мурманск (8152)59-64-93	Саратов (845)249-38-78	Челябинск (351)202-03-61
Ижевск (3412)26-03-58	Набережные Челны (8552)20-53-41	Севастополь (8692)22-31-93	Череповец (8202)49-02-64
Иркутск (395)279-98-46	Нижний Новгород (831)429-08-12	Симферополь (3652)67-13-56	Ярославль (4852)69-52-93
Россия (495)268-04-70	Киргизия (996)312-96-26-47	Казахстан (7172)727-132	










## MODULES - MODULES, BRIDGE










Series	Product Image	$I_o$ (A)	$T_c$ (°C)	$V_{RRM}$ (V)	$V_{RSM}$ (V)	$I_{FSM}$ at 50 Hz (A)	$I_{FSM}$ at 60 Hz (A)	$V_{FM}$ at $I_{TM}$ at 25 °C (V)	$T_J$ (°C)	$R_{thJC}$ per Junction (°C/W)	Package	Circuit Configuration
VS-104MT..KPbF Series		100	80	1600	1700	1130	1180	1.53 at 150	125	0.69	MTK	Three phase AC switch
VS-104MT..KPbF Series		100	80	800	900	1130	1180	1.53 at 150	125	0.69	MTK	Three phase AC switch
VS-104MT..KPbF Series		100	80	1200	1300	1130	1180	1.53 at 150	125	0.69	MTK	Three phase AC switch
VS-104MT..KPbF Series		100	80	1400	1500	1130	1180	1.53 at 150	125	0.69	MTK	Three phase AC switch
VS-104MT..KPbF Series		100	80	1000	1100	1130	1180	1.53 at 150	125	0.69	MTK	Three phase AC switch
VS-130-160MT..KPbF Series		160	85	1200	1300	1430	1500	1.49 at 200	150	0.73	MTK	Three phase bridge
VS-130-160MT..KPbF Series		160	85	800	900	1430	1500	1.49 at 200	150	0.73	MTK	Three phase bridge
VS-130-160MT..KPbF Series		130	85	1400	1500	1130	1180	1.63 at 200	150	0.93	MTK	Three phase bridge
VS-130-160MT..KPbF Series		130	85	1000	1100	1130	1180	1.63 at 200	150	0.93	MTK	Three phase bridge











Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-130-160MT..KPbF Series		160	85	1400	1500	1430	1500	1.49 at 200	150	0.73	MTK	Three phase bridge
VS-130-160MT..KPbF Series		160	85	1000	1100	1430	1500	1.49 at 200	150	0.73	MTK	Three phase bridge
VS-130-160MT..KPbF Series		130	85	1600	1700	1130	1180	1.63 at 200	150	0.93	MTK	Three phase bridge
VS-130-160MT..KPbF Series		130	85	1200	1300	1130	1180	1.63 at 200	150	0.93	MTK	Three phase bridge
VS-130-160MT..KPbF Series		160	85	1600	1700	1430	1500	1.49 at 200	150	0.73	MTK	Three phase bridge
VS-130-160MT..KPbF Series		130	85	800	900	1130	1180	1.63 at 200	150	0.93	MTK	Three phase bridge
VS-130MT...C Series		218	85	1600	1700	1270	1330	2.05 at 300	150	0.41	MTC	Three phase bridge
VS-130MT...C Series		218	85	1800	1900	1270	1330	2.05 at 300	150	0.41	MTC	Three phase bridge
VS-131MT...C Series		218	85	1600	1700	1270	1330	2.05 at 300	150	0.41	MTC	Three phase bridge
VS-131MT...C Series		218	85	1800	1900	1270	1330	2.05 at 300	150	0.41	MTC	Three phase bridge
VS-160MT...C Series		257	85	1600	1700	1540	1610	1.85 at 300	150	0.35	MTC	Three phase bridge

Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-160MT...C Series		257	85	1800	1900	1540	1610	1.85 at 300	150	0.35	MTC	Three phase bridge
VS-161MT...C Series		257	85	1600	1700	1540	1610	1.85 at 300	150	0.35	MTC	Three phase bridge
VS-161MT...C Series		257	85	1800	1900	1540	1610	1.85 at 300	150	0.35	MTC	Three phase bridge
VS-1KAB-E Series		1.2	n/a	800	800	50	52	1.1 at 1.2	150	n/a	D-38	Single phase bridge
VS-1KAB-E Series		1.2	n/a	400	400	50	52	1.1 at 1.2	150	n/a	D-38	Single phase bridge
VS-1KAB-E Series		1.2	n/a	100	100	50	52	1.1 at 1.2	150	n/a	D-38	Single phase bridge
VS-1KAB-E Series		1.2	n/a	1000	1000	50	52	1.1 at 1.2	150	n/a	D-38	Single phase bridge
VS-1KAB-E Series		1.2	n/a	600	600	50	52	1.1 at 1.2	150	n/a	D-38	Single phase bridge
VS-1KAB-E Series		1.2	n/a	200	200	50	52	1.1 at 1.2	150	n/a	D-38	Single phase bridge
VS-1KAB-E Series		1.2	n/a	50	50	50	52	1.1 at 1.2	150	n/a	D-38	Single phase bridge


Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-200MT40KPbF		200	85	400	500	1800	1880	1.40 at 200	150	0.69	MTK	Three phase bridge
VS-26MT.., VS-36MT.. Series		25	70	600	725	360	375	1.26 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		35	60	100	150	475	500	1.19 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		35	60	1400	1500	475	500	1.19 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		25	70	200	275	360	375	1.26 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		25	70	1600	1700	360	375	1.26 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		35	60	1000	1100	475	500	1.19 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		25	70	50	75	360	375	1.26 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		25	70	1200	1300	360	375	1.26 at 40	150	n/a	D-63	Three phase bridge





Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-26MT.., VS-36MT.. Series		35	60	600	725	475	500	1.19 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		25	70	800	900	360	375	1.26 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		35	60	200	275	475	500	1.19 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		25	70	400	500	360	375	1.26 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		35	60	1600	1700	475	500	1.19 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		35	60	50	75	475	500	1.19 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		35	60	1200	1300	475	500	1.19 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		25	70	100	150	360	375	1.26 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		25	70	1400	1500	360	375	1.26 at 40	150	n/a	D-63	Three phase bridge



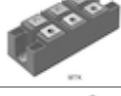
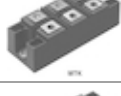

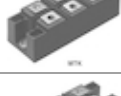
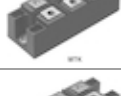
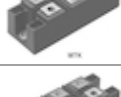
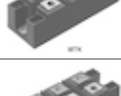

Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-26MT.., VS-36MT.. Series		35	60	800	900	475	500	1.19 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		25	70	1000	1100	360	375	1.26 at 40	150	n/a	D-63	Three phase bridge
VS-26MT.., VS-36MT.. Series		35	60	400	500	475	500	1.19 at 40	150	n/a	D-63	Three phase bridge
VS-2KBB Series		1.9	45	800	800	50	52	1.1 at 1.9	150	n/a	2KBB	Single phase bridge
VS-2KBB Series		1.9	45	400	400	50	52	1.1 at 1.9	150	n/a	2KBB	Single phase bridge
VS-2KBB Series		1.9	45	100	100	50	52	1.1 at 1.9	150	n/a	2KBB	Single phase bridge
VS-2KBB Series		1.9	45	1000	1000	50	52	1.1 at 1.9	150	n/a	2KBB	Single phase bridge
VS-2KBB Series		1.9	45	600	600	50	52	1.1 at 1.9	150	n/a	2KBB	Single phase bridge
VS-2KBB Series		1.9	45	200	200	50	52	1.1 at 1.9	150	n/a	2KBB	Single phase bridge









Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-2KBB Series		1.9	45	50	50	50	52	1.1 at 1.9	150	n/a	2KBB	Single phase bridge
VS-2KBP Series		2	n/a	800	800	60	63	1.0 at 1	150	n/a	D-44	Single phase bridge
VS-2KBP Series		2	n/a	400	400	60	63	1.0 at 1	150	n/a	D-44	Single phase bridge
VS-2KBP Series		2	n/a	100	100	60	63	1.0 at 1	150	n/a	D-44	Single phase bridge
VS-2KBP Series		2	n/a	1000	1000	60	63	1.0 at 1	150	n/a	D-44	Single phase bridge
VS-2KBP Series		2	n/a	600	600	60	63	1.0 at 1	150	n/a	D-44	Single phase bridge
VS-2KBP Series		2	n/a	200	200	60	63	1.0 at 1	150	n/a	D-44	Single phase bridge
VS-2KBP Series		2	n/a	50	50	60	63	1.0 at 1	150	n/a	D-44	Single phase bridge
VS-300MT...C Series		258	110	1600	1700	2400	2512	1.54 at 240	150	0.23	MTC	Three phase bridge
VS-300MT...C Series		258	110	1800	1900	2400	2512	1.54 at 240	150	0.23	MTC	Three phase bridge


























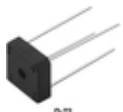
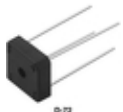
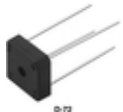
Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-301MT...C Series		258	110	1600	1700	2400	2512	1.54 at 240	150	0.23	MTC	Three phase bridge
VS-301MT...C Series		258	110	1800	1900	2400	2512	1.54 at 240	150	0.23	MTC	Three phase bridge
VS-40MT160KPbF		40	85	1600	1700	270	280	2.02 at 100	150	2.46	MTK	Three phase bridge
VS-40MT160P-P, VS-70MT160P-P, VS-100MT160P-P		100	80	1600	1700	450	470	1.51 at 100	150	1.14	MTP PressFit	Three phase bridge
VS-40MT160P-P, VS-70MT160P-P, VS-100MT160P-P		45	100	1600	1700	270	280	1.45 at 40	150	1.6	MTP PressFit	Three phase bridge
VS-40MT160P-P, VS-70MT160P-P, VS-100MT160P-P		75	80	1600	1700	380	398	1.45 at 70	150	1.38	MTP PressFit	Three phase bridge
VS-40MT160P.PbF, VS-70MT160P.PbF, VS-100MT160P.PbF Series		75	80	1600	1700	380	398	1.45 at 70	150	1.38	MTP	Three phase bridge
VS-40MT160P.PbF, VS-70MT160P.PbF, VS-100MT160P.PbF Series		100	80	1600	1700	450	470	1.51 at 100	150	1.14	MTP	Three phase bridge
VS-40MT160P.PbF, VS-70MT160P.PbF, VS-100MT160P.PbF Series		45	100	1600	1700	270	280	1.45 at 40	150	1.6	MTP	Three phase bridge
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		110	85	1600	1700	1130	1180	1.57 at 150	125	0.70	MTK	Three phase bridge
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		90	85	800	900	950	1000	1.65 at 150	125	0.86	MTK	Three phase bridge
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		110	85	1200	1300	1130	1180	1.57 at 150	125	0.70	MTK	Three phase bridge

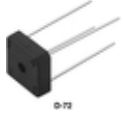
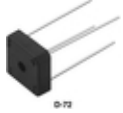
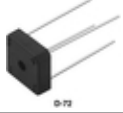
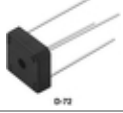
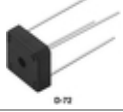
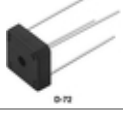
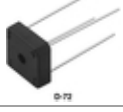
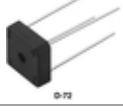
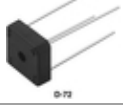
Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		55	85	1400	1500	390	410	2.68 at 150	125	1.07	MTK	Three phase bridge
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		110	85	800	900	1130	1180	1.57 at 150	125	0.70	MTK	Three phase bridge
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		55	85	1000	1100	390	410	2.68 at 150	125	1.07	MTK	Three phase bridge
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		90	85	1400	1500	950	1000	1.65 at 150	125	0.86	MTK	Three phase bridge
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		90	85	1000	1100	950	1000	1.65 at 150	125	0.86	MTK	Three phase bridge
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		110	85	1400	1500	1130	1180	1.57 at 150	125	0.70	MTK	Three phase bridge
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		55	85	1600	1700	390	410	2.68 at 150	125	1.07	MTK	Three phase bridge
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		110	85	1000	1100	1130	1180	1.57 at 150	125	0.70	MTK	Three phase bridge
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		55	85	1200	1300	390	410	2.68 at 150	125	1.07	MTK	Three phase bridge
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		90	85	1600	1700	950	1000	1.65 at 150	125	0.86	MTK	Three phase bridge
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		55	85	800	900	390	410	2.68 at 150	125	1.07	MTK	Three phase bridge

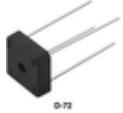
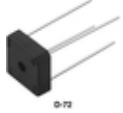

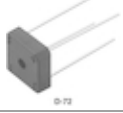
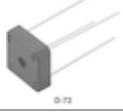
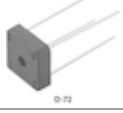
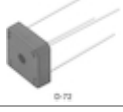
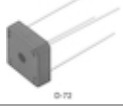
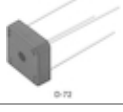
Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-5.MT...KPbF, VS-9.MT...KPbF, VS-11.MT...KPbF Series		90	85	1200	1300	950	1000	1.65 at 150	125	0.86	MTK	Three phase bridge
VS-60-70MT..KPbF Series		60	85	1400	1500	420	440	1.75 at 100	150	2.22	MTK	Three phase bridge
VS-60-70MT..KPbF Series		60	85	1000	1100	420	440	1.75 at 100	150	2.22	MTK	Three phase bridge
VS-60-70MT..KPbF Series		70	85	1400	1500	480	500	1.55 at 100	150	1.75	MTK	Three phase bridge
VS-60-70MT..KPbF Series		70	85	1000	1100	480	500	1.55 at 100	150	1.75	MTK	Three phase bridge
VS-60-70MT..KPbF Series		60	85	1600	1700	420	440	1.75 at 100	150	2.22	MTK	Three phase bridge
VS-60-70MT..KPbF Series		60	85	1200	1300	420	440	1.75 at 100	150	2.22	MTK	Three phase bridge
VS-60-70MT..KPbF Series		70	85	1600	1700	480	500	1.55 at 100	150	1.75	MTK	Three phase bridge
VS-60-70MT..KPbF Series		60	85	800	900	420	440	1.75 at 100	150	2.22	MTK	Three phase bridge
VS-60-70MT..KPbF Series		70	85	1200	1300	480	500	1.55 at 100	150	1.75	MTK	Three phase bridge
VS-60-70MT..KPbF Series		70	85	800	900	480	500	1.55 at 100	150	1.75	MTK	Three phase bridge

Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-90-110MT.KPbF Series		90	90	1000	1100	770	810	1.6 at 150	150	1.26	MTK	Three phase bridge
VS-90-110MT.KPbF Series		110	90	1400	1500	950	1000	1.4 at 150	150	1.07	MTK	Three phase bridge
VS-90-110MT.KPbF Series		110	90	1000	1100	950	1000	1.4 at 150	150	1.07	MTK	Three phase bridge
VS-90-110MT.KPbF Series		90	90	1600	1700	770	810	1.6 at 150	150	1.26	MTK	Three phase bridge
VS-90-110MT.KPbF Series		90	90	1200	1300	770	810	1.6 at 150	150	1.26	MTK	Three phase bridge
VS-90-110MT.KPbF Series		110	90	1600	1700	950	1000	1.4 at 150	150	1.07	MTK	Three phase bridge
VS-90-110MT.KPbF Series		90	90	800	900	770	810	1.6 at 150	150	1.26	MTK	Three phase bridge
VS-90-110MT.KPbF Series		110	90	1200	1300	950	1000	1.4 at 150	150	1.07	MTK	Three phase bridge
VS-90-110MT.KPbF Series		110	90	800	900	950	1000	1.4 at 150	150	1.07	MTK	Three phase bridge
VS-90-110MT.KPbF Series		90	90	1400	1500	770	810	1.6 at 150	150	1.26	MTK	Three phase bridge
VS-GBPC.. Series		25	60	200	275	400	420	n/a	150	1.7	GBPC...W	Single phase bridge










Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-GBPC.. Series		25	60	400	500	400	420	n/a	150	1.7	GBPC...A	Single phase bridge
VS-GBPC.. Series		35	55	400	500	475	500	n/a	150	1.4	GBPC...W	Single phase bridge
VS-GBPC.. Series		35	55	1000	1100	475	500	n/a	150	1.4	GBPC...A	Single phase bridge
VS-GBPC.. Series		35	55	200	275	475	500	n/a	150	1.4	GBPC...A	Single phase bridge
VS-GBPC.. Series		25	60	1200	1300	400	420	n/a	150	1.7	GBPC...W	Single phase bridge
VS-GBPC.. Series		35	55	600	725	475	500	n/a	150	1.4	GBPC...A	Single phase bridge
VS-GBPC.. Series		25	60	1000	1100	400	420	n/a	150	1.7	GBPC...A	Single phase bridge
VS-GBPC.. Series		25	60	800	900	400	420	n/a	150	1.7	GBPC...W	Single phase bridge
VS-GBPC.. Series		35	55	1000	1100	475	500	n/a	150	1.4	GBPC...W	Single phase bridge
VS-GBPC.. Series		25	60	400	500	400	420	n/a	150	1.7	GBPC...W	Single phase bridge
VS-GBPC.. Series		25	60	600	725	400	420	n/a	150	1.7	GBPC...A	Single phase bridge
VS-GBPC.. Series		35	55	600	725	475	500	n/a	150	1.4	GBPC...W	Single phase bridge
VS-GBPC.. Series		35	55	1200	1300	475	500	n/a	150	1.4	GBPC...A	Single phase bridge
VS-GBPC.. Series		25	60	200	275	400	420	n/a	150	1.7	GBPC...A	Single phase bridge










Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-GBPC.. Series		35	55	400	500	475	500	n/a	150	1.4	GBPC...A	Single phase bridge
VS-GBPC.. Series		35	55	200	275	475	500	n/a	150	1.4	GBPC...W	Single phase bridge
VS-GBPC.. Series		35	55	800	900	475	500	n/a	150	1.4	GBPC...A	Single phase bridge
VS-GBPC.. Series		25	60	1200	1300	400	420	n/a	150	1.7	GBPC...A	Single phase bridge
VS-GBPC.. Series		25	60	1000	1100	400	420	n/a	150	1.7	GBPC...W	Single phase bridge
VS-GBPC.. Series		35	55	1200	1300	475	500	n/a	150	1.4	GBPC...W	Single phase bridge
VS-GBPC.. Series		25	60	600	725	400	420	n/a	150	1.7	GBPC...W	Single phase bridge
VS-GBPC.. Series		25	60	800	900	400	420	n/a	150	1.7	GBPC...A	Single phase bridge
VS-GBPC.. Series		35	55	800	900	475	500	n/a	150	1.4	GBPC...W	Single phase bridge
VS-KBPC1, VS-KBPC6 Series		3	50	50	50	50	55	n/a	150	n/a	D-72	Single phase bridge
VS-KBPC1, VS-KBPC6 Series		6	50	50	50	125	137	n/a	150	n/a	D-72	Single phase bridge
VS-KBPC1, VS-KBPC6 Series		3	50	800	800	50	55	n/a	150	n/a	D-72	Single phase bridge

Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-KBPC1, VS-KBPC6 Series		6	50	800	800	125	137	n/a	150	n/a	D-72	Single phase bridge
VS-KBPC1, VS-KBPC6 Series		3	50	400	400	50	55	n/a	150	n/a	D-72	Single phase bridge
VS-KBPC1, VS-KBPC6 Series		6	50	400	400	125	137	n/a	150	n/a	D-72	Single phase bridge
VS-KBPC1, VS-KBPC6 Series		3	50	100	100	50	55	n/a	150	n/a	D-72	Single phase bridge
VS-KBPC1, VS-KBPC6 Series		6	50	100	100	125	137	n/a	150	n/a	D-72	Single phase bridge
VS-KBPC1, VS-KBPC6 Series		3	50	1000	1000	50	55	n/a	150	n/a	D-72	Single phase bridge
VS-KBPC1, VS-KBPC6 Series		6	50	1000	1000	125	137	n/a	150	n/a	D-72	Single phase bridge
VS-KBPC1, VS-KBPC6 Series		3	50	600	600	50	55	n/a	150	n/a	D-72	Single phase bridge
VS-KBPC1, VS-KBPC6 Series		6	50	600	600	125	137	n/a	150	n/a	D-72	Single phase bridge

Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-KBPC1, VS-KBPC6 Series		3	50	200	200	50	55	n/a	150	n/a	D-72	Single phase bridge
VS-KBPC1, VS-KBPC6 Series		6	50	200	200	125	137	n/a	150	n/a	D-72	Single phase bridge
VS-KBPC8 Series		8	50	200	300	125	137	1.0 at 3.0	150	6	D-72	Single phase bridge
VS-KBPC8 Series		8	50	50	80	125	137	1.0 at 3.0	150	6	D-72	Single phase bridge
VS-KBPC8 Series		8	50	800	900	125	137	1.0 at 3.0	150	6	D-72	Single phase bridge
VS-KBPC8 Series		8	50	400	500	125	137	1.0 at 3.0	150	6	D-72	Single phase bridge
VS-KBPC8 Series		8	50	100	150	125	137	1.0 at 3.0	150	6	D-72	Single phase bridge
VS-KBPC8 Series		8	50	1000	1100	125	137	1.0 at 3.0	150	6	D-72	Single phase bridge
VS-KBPC8 Series		8	50	600	700	125	137	1.0 at 3.0	150	6	D-72	Single phase bridge





















Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-MB High Voltage Series		25	70	1400	1500	400	420	1.25 at 40	150	1.7	D-34	Single phase bridge
VS-MB High Voltage Series		35	55	1600	1700	475	500	1.3 at 55	150	1.35	D-34	Single phase bridge
VS-MB High Voltage Series		25	70	1600	1700	400	420	1.25 at 40	150	1.7	D-34	Single phase bridge
VS-MB High Voltage Series		35	55	1400	1500	475	500	1.3 at 55	150	1.35	D-34	Single phase bridge
VS-MB Series		25	65	1000	1100	400	420	1.11 at 40	150	1.7	D-34	Single phase bridge
VS-MB Series		25	65	200	275	400	420	1.11 at 40	150	1.7	D-34	Single phase bridge
VS-MB Series		35	60	600	725	475	500	1.14 at 55	150	1.2	D-34	Single phase bridge
VS-MB Series		25	65	600	725	400	420	1.11 at 40	150	1.7	D-34	Single phase bridge
VS-MB Series		25	65	60	100	400	420	1.11 at 40	150	1.7	D-34	Single phase bridge








Series	Product Image	I <sub>O</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	V <sub>RSM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>FM</sub> at I <sub>TM</sub> at 25 °C (V)	T <sub>J</sub> (°C)	R <sub>thJC</sub> per Junction (°C/W)	Package	Circuit Configuration
VS-MB Series		35	60	200	275	475	500	1.14 at 55	150	1.2	D-34	Single phase bridge
VS-MB Series		35	60	60	100	475	500	1.14 at 55	150	1.2	D-34	Single phase bridge
VS-MB Series		35	60	1200	1300	475	500	1.14 at 55	150	1.2	D-34	Single phase bridge
VS-MB Series		25	65	1200	1300	400	420	1.11 at 40	150	1.7	D-34	Single phase bridge
VS-MB Series		35	60	800	900	475	500	1.14 at 55	150	1.2	D-34	Single phase bridge
VS-MB Series		25	65	800	900	400	420	1.11 at 40	150	1.7	D-34	Single phase bridge
VS-MB Series		25	65	100	150	400	420	1.11 at 40	150	1.7	D-34	Single phase bridge
VS-MB Series		35	60	400	500	475	500	1.14 at 55	150	1.2	D-34	Single phase bridge
VS-MB Series		25	65	400	500	400	420	1.11 at 40	150	1.7	D-34	Single phase bridge

Series	Product Image	$I_O$ (A)	$T_C$ (°C)	$V_{RRM}$ (V)	$V_{RSM}$ (V)	$I_{FSM}$ at 50 Hz (A)	$I_{FSM}$ at 60 Hz (A)	$V_{FM}$ at $I_{TM}$ at 25 °C (V)	$T_J$ (°C)	$R_{thJC}$ per Junction (°C/W)	Package	Circuit Configuration
VS-MB Series		35	60	100	150	475	500	1.14 at 55	150	1.2	D-34	Single phase bridge
VS-MB Series		25	65	50	75	400	420	1.11 at 40	150	1.7	D-34	Single phase bridge
VS-MB Series		35	60	50	75	475	500	1.14 at 55	150	1.2	D-34	Single phase bridge
VS-MB Series		35	60	1000	1100	475	500	1.14 at 55	150	1.2	D-34	Single phase bridge
VS-SA61BA60		61	57	600	700	300	310	1.33 at 30	150	1.2	SOT-227	Single phase bridge
VS-UFH60BA65		60	123	650	700	360	377	2.35 at 60	175	0.91	SOT-227	Single phase bridge








## MODULES - MODULES, DIODE - FRED PT®

Series	Product Image	$I_{FAV}$ (A)	$T_C$ (°C)	$V_{RRM}$ (V)	$I_{FSM}$ (A)	$V_F$ at $I_{FM}$ at 25 °C (V at A)	$T_J$ (°C)	$R_{thJC}$ (°C/W)	$t_{rr}$ (ns)	Package	Circuit Configuration	Isolation from Heatsink
VS-402CNQ200PBF		551	175	200	2100	0.845 at 200	-40 to +175	0.19	132	TO-244	Two diodes common cathode	Not Isolated
VS-U5FH120FA120		60	85	1200	460	1.99 at 60	-55 to +175	0.69	71	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-U5FH120FA60		60	115	600	550	1.36 at 60	-55 to +175	0.7	67	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-U5FH150FA60		75	100	600	630	1.34 at 75	-55 to +175	0.54	70	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-U5FH240FA120		120	75	1200	720	1.99 at 120	-55 to +175	0.38	98	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-U5FH300FA60		150	98	600	1080	1.36 at 150	-55 to +175	0.35	76	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-U5FH30BA60		30	180	600	360	1.6 at 30	-55 to +175	1.39	57	SOT-227	Single phase bridge	Isolated
VS-U5FH60FA120		30	105	1200	250	2.08 at 30	-55 to +175	1.1	54	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-U5FH60FA60		30	132	600	370	1.37 at 30	-55 to +175	0.95	61	SOT-227	Two separate diodes, parallel pin-out	Isolated

Series	Product Image	$I_{FAV}$ (A)	$T_C$ (°C)	$V_{RRM}$ (V)	$I_{FSM}$ (A)	$V_F$ at $I_{FM}$ at 25 °C (V at A)	$T_J$ (°C)	$R_{thJC}$ (°C/W)	$t_{rr}$ (ns)	Package	Circuit Configuration	Isolation from Heatsink
VS-U5FX120FA120		60	70	1200	385	2.75 at 60	-55 to +175	0.69	46	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-U5FX120FA60		60	100	600	530	1.6 at 60	-55 to +175	0.7	63	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-U5FX150FA60		75	102	600	650	1.6 at 75	-55 to +175	0.54	65	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-U5FX240FA120		120	59	1200	690	2.8 at 120	-55 to +175	0.38	60	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-U5FX300FA60		150	80	600	1035	1.6 at 150	-55 to +175	0.35	72	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-U5FX60FA120		30	85	1200	180	2.91 at 30	-55 to +175	1.1	41	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-U5FX60FA60		30	128	600	340	1.6 at 30	-55 to +175	0.95	57	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFB130FA20		65	132	200	890	0.96 at 60	-55 to +175	0.72	42	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFB130FA40		65	123	400	800	1.16 at 60	-55 to +175	0.72	86	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFB130FA60		82	85	600	750	1.43 at 60	-55 to +175	0.73	79	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFB170FA60		94	90	600	850	1.02 at 50	-55 to +175	0.73	170	SOT-227	Two separate diodes, parallel pin-out	Isolated










Series	Product Image	$I_{FAV}$ (A)	$T_C$ (°C)	$V_{RRM}$ (V)	$I_{FSM}$ (A)	$V_F$ at $I_{FM}$ at 25 °C (V at A)	$T_J$ (°C)	$R_{thJC}$ (°C/W)	$t_{rr}$ (ns)	Package	Circuit Configuration	Isolation from Heatsink
VS-UFB201FA40		120	88	400	600	1.33 at 100	-55 to +175	0.56	80	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFB211FA40		210	90	400	1300	1.06 at 100	-55 to +175	0.32	93	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFB230FA60		141	85	600	1400	1.46 at 100	-55 to +175	0.43	83	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFB250FA60		168	90	600	1300	1.02 at 100	-55 to +175	0.43	166	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFB280FA20		175	95	200	1700	1.0 at 120	-55 to +175	0.43	34	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFB280FA40		170	90	400	1300	1.06 at 100	-55 to +175	0.43	93	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFB310CB40		155	135	400	1300	1.11 at 100	-55 to +175	0.19	89	SOT-227 not insulated	Two diodes common cathode	Not Isolated
VS-UFB80FA20		40	137	200	280	0.96 at 30	-55 to +175	1	34	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFB80FA40		40	130	400	270	1.14 at 30	-55 to +175	1	68	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFB80FA60		57	85	600	280	1.32 at 30	-55 to +175	1.02	79	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFH280FA30		160	95	300	1539	1.07 at 100	-55 to +175	0.39	58	SOT-227	Two separate diodes, parallel pin-out	Isolated











Series	Product Image	$I_{FAV}$ (A)	$T_C$ (°C)	$V_{RRM}$ (V)	$I_{FSM}$ (A)	$V_F$ at $I_{FM}$ at 25 °C (V at A)	$T_J$ (°C)	$R_{thJC}$ (°C/W)	$t_{rr}$ (ns)	Package	Circuit Configuration	Isolation from Heatsink
VS-UFL130FA60		87	85	600	800	1.29 at 60	-55 to +175	0.73	105	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFL230FA60		160	85	600	1500	1.28 at 100	-55 to +175	0.43	104	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFL250CB60		130	135	600	1500	1.28 at 100	-55 to +175	0.19	104	SOT-227 not insulated	Two diodes common cathode	Not Isolated
VS-UFL330FA60		243	90	600	1130	1.43 at 200	-55 to +175	0.22	98	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-UFL450CB60		250	135	600	1170	1.18 at 100	-55 to +175	0.11	97	SOT-227	Two diodes common cathode	Not Isolated
VS-UFL80FA60		65	85	600	300	1.1 at 30	-55 to +175	1.02	115	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-VS5HD240CW60		229	115	600	1300	1.52 at 120	-40 to +175	0.38	52	TO-244	Two diodes common cathode	Not Isolated
VS-VS5HD300CW60		259	109	600	1150	1.36 at 150	-40 to +175	0.32	68	TO-244	Two diodes common cathode	Not Isolated
VS-VS5HD480CW60		448	113	600	2200	1.5 at 240	-40 to +175	0.19	61	TO-244	Two diodes common cathode	Not Isolated
VS-VS5HD600CW60		505	106	600	2200	1.34 at 300	-40 to +175	0.16	78	TO-244	Two diodes common cathode	Not Isolated
VS-VSKDF400/06PbF		540	90	600	4140	1.28 at 200	-40 to +175	0.147	159	INT-A-PAK	Diode doubler circuit	Isolated

Series	Product Image	$I_{FAV}$ (A)	$T_C$ (°C)	$V_{RRM}$ (V)	$I_{FSM}$ (A)	$V_F$ at $I_{FM}$ at 25 °C (V at A)	$T_J$ (°C)	$R_{thJC}$ (°C/W)	$t_{rr}$ (ns)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSKDF500/06PbF		772	90	600	4140	1.25 at 250	-40 to +175	0.11	104	INT-A-PAK	Diode doubler circuit	Isolated
VS-VSKDF600/06PbF		771	90	600	4140	1.305 at 300	-40 to +175	0.088	150	INT-A-PAK	Diode doubler circuit	Isolated
VS-VSKEF500/06PbF		772	90	600	4500	1.25 at 250	-40 to +175	0.11	104	INT-A-PAK	Single diode	Isolated
VS-VSUD360CW40		510	116	400	2880	1.09 at 180	-40 to +150	0.19	74	TO-244	Two diodes common cathode	Not Isolated
VS-VSUD400CW20		240	127	200	2300	0.94 at 200	-40 to +150	0.19	87	TO-244 module	Two diodes common cathode	Not Isolated
VS-VSUD400CW60		330	97	600	2520	1.45 at 200	-40 to +150	0.19	90	TO-244	Two diodes common cathode	Not Isolated
VS-VSUD405CW60		480	132	600	2880	1.13 at 200	-40 to +175	0.19	124	TO-244	Two diodes common cathode	Not Isolated
VS-VSUD410CW60		572	137	600	3330	1.0 at 200	-40 to +175	0.19	215	TO-244	Two diodes common cathode	Not Isolated



## MODULES - MODULES, DIODE - HEXFRED®








Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	t <sub>rr</sub> (ns)	Package	Circuit Configuration	Isolation from Heatsink
VS-35MT120PB		30	88	1200	300	2.46 at 30	-40 to +150	0.18	121	MTP	Single phase bridge	Isolated
VS-HFA135NH40PbF		138	100	400	900	1.06 at 135	-55 to +150	0.27	77	HALF-PAK (D-67)	Single diode	Not Isolated
VS-HFA140FA120		140	74	1200	350	2.8 at 60	-55 to +150	0.35	145	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-HFA140FA60		140	110	600	600	1.33 at 60	-55 to +150	0.35	90	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-HFA140NJ60CPbF		84	100	600	400	1.37 at 70	-55 to +150	0.38	80	TO-244	Two diodes common cathode	Not Isolated
VS-HFA180NH40PbF		200	100	400	1200	1.08 at 180	-55 to +150	0.19	90	HALF-PAK (D-67)	Single diode	Not Isolated
VS-HFA210NJ60CPbF		120	100	600	600	1.38 at 105	-55 to +150	0.27	90	TO-244	Two diodes common cathode	Not Isolated
VS-HFA220FA120		110	68	1200	700	2.68 at 100	-55 to +150	0.25	157	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-HFA240NJ40CPbF		197	100	400	900	1.1 at 120	-55 to +150	0.19	77	TO-244	Two diodes common cathode	Not Isolated







Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	t <sub>rr</sub> (ns)	Package	Circuit Configuration	Isolation from Heatsink
VS-HFA280NJ60CPbF		149	100	600	600	1.33 at 105	-55 to +150	0.19	92	TO-244	Two diodes common cathode	Not Isolated
VS-HFA320NJ40CPbF		255	85	400	1200	1.10 at 160	-55 to +150	0.19	90	TO-244	Two diodes common cathode	Not Isolated
VS-HFA70EA120		35	121	1200	350	2.2 at 30	-55 to +150	0.35	145	SOT-227	Two separate diodes, antiparallel pin-out	Isolated
VS-HFA70FA120		35	110	1200	380	2.30 at 30	-55 to +150	0.46	134	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-HFA90FA120		45	83	1200	400	2.46 at 25	-55 to +150	0.48	80	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-HFA90NH40PbF		106	100	400	600	1.06 at 90	-55 to +150	0.38	90	HALF-PAK (D-67)	Single diode	Not Isolated
VS-VSKCU300/06PbF		230	100	600	n/a	1.23 at 150	-40 to +150	0.16	130	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSKDU162/12PbF		110	100	1200	800	2.5 at 100	-40 to +150	0.18	150	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSKDU300/06PbF		230	100	600	n/a	1.23 at 150	-40 to +150	0.16	130	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSKEU300/12PbF		300	60	1200	2400	2.18 at 300	-40 to +150	0.12	233	INT-A-PAK	Single diode	Isolated

## MODULES - MODULES, DIODE - HIGH PERFORMANCE SCHOTTKY



Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-120NQ045PbF	 HALF-PAK (D-67)	120	105	45	1550	0.63 at 120	-55 to +150	0.38	HALF-PAK (D-67)	Single diode	Not Isolated
VS-121NQ045PbF	 HALF-PAK (D-67)	120	137	45	2000	0.65 at 120	-55 to +175	0.38	HALF-PAK (D-67)	Single diode	Not Isolated
VS-122NQ030PbF	 HALF-PAK (D-67)	120	115	30	2000	0.57 at 120	-55 to +150	0.38	HALF-PAK (D-67)	Single diode	Not Isolated
VS-123NQ100PbF	 HALF-PAK (D-67)	120	133	100	1800	0.91 at 120	-55 to +175	0.38	HALF-PAK (D-67)	Single diode	Not Isolated
VS-125NQ015PbF	 HALF-PAK (D-67)	120	74	15	1700	0.43 at 120	-55 to +125	0.38	HALF-PAK (D-67)	Single diode	Not Isolated
VS-180NQ045PbF	 HALF-PAK (D-67)	180	105	45	2400	0.6 at 180	-55 to +150	0.28	HALF-PAK (D-67)	Single diode	Not Isolated
VS-182NQ030PbF	 HALF-PAK (D-67)	180	108	30	2500	0.59 at 180	-55 to +150	0.28	HALF-PAK (D-67)	Single diode	Not Isolated
VS-183NQ100PbF	 HALF-PAK (D-67)	180	128	100	2500	0.91 at 180	-55 to +175	0.28	HALF-PAK (D-67)	Single diode	Not Isolated

Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-200CNQ045PbF		200	116	45	1550	0.55 at 100	-55 to +150	0.38	TO-244	Two diodes common cathode	Not Isolated
VS-201CNQ045PbF		200	146	45	2000	0.67 at 100	-55 to +175	0.38	TO-244	Two diodes common cathode	Not Isolated
VS-201CNQ050PbF		200	146	50	2000	0.67 at 100	-55 to +175	0.38	TO-244	Two diodes common cathode	Not Isolated
VS-203CNQ100PbF		200	142	100	1700	0.86 at 100	-55 to +175	0.38	TO-244	Two diodes common cathode	Not Isolated
VS-209CNQ...PbF Series		200	131	135	1200	1.06 at 100	-55 to +175	0.38	TO-244	Two diodes common cathode	Not Isolated
VS-209CNQ...PbF Series		200	131	150	1200	1.06 at 100	-55 to +175	0.38	TO-244	Two diodes common cathode	Not Isolated
VS-220CNQ030PbF		220	122	30	1950	0.49 at 110	-55 to +150	0.38	TO-244	Two diodes common cathode	Not Isolated
VS-240NQ045PbF	 HALF-PAK (D-67)	240	104	45	3400	0.72 at 240	-55 to +150	0.19	HALF-PAK (D-67)	Single diode	Not Isolated
VS-241NQ045PbF	 HALF-PAK (D-67)	240	144	45	3450	0.8 at 240	-55 to +175	0.19	HALF-PAK (D-67)	Single diode	Not Isolated
VS-242NQ030PbF	 HALF-PAK (D-67)	240	118	30	3000	0.54 at 240	-55 to +150	0.19	HALF-PAK (D-67)	Single diode	Not Isolated
VS-243NQ100PbF	 HALF-PAK (D-67)	240	132	100	3300	0.95 at 240	-55 to +175	0.19	HALF-PAK (D-67)	Single diode	Not Isolated

Series	Product Image	$I_{FAV}$ (A)	$T_C$ (°C)	$V_{RRM}$ (V)	$I_{FSM}$ (A)	$V_F$ at $I_{FM}$ at 25 °C (V at A)	$T_J$ (°C)	$R_{thJC}$ (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-245NQ015PbF		240	73	15	3000	0.52 at 240	-55 to +125	0.19	HALF-PAK (D-67)	Single diode	Not Isolated
VS-249NQ150PbF		240	121	150	2300	1.21 at 240	-55 to +175	0.19	HALF-PAK (D-67)	Single diode	Not Isolated
VS-300CNQ045PbF		300	111	45	2400	0.61 at 150	-55 to +150	0.28	TO-244	Two diodes common cathode	Not Isolated
VS-301CNQ...PbF Series		300	132	40	3200	0.69 at 150	-55 to +175	0.28	TO-244	Two diodes common cathode	Not Isolated
VS-301CNQ...PbF Series		300	132	45	3200	0.69 at 150	-55 to +175	0.28	TO-244	Two diodes common cathode	Not Isolated
VS-303CNQ100PbF		300	138	100	2500	0.91 at 150	-55 to +175	0.28	TO-244	Two diodes common cathode	Not Isolated
VS-400CNQ045PbF		400	114	45	3400	0.57 at 200	-55 to +150	0.19	TO-244	Two diodes common cathode	Not Isolated
VS-401CNQ...PbF Series		400	147	45	3450	0.67 at 200	-55 to +175	0.19	TO-244	Two diodes common cathode	Not Isolated
VS-401CNQ...PbF Series		400	147	40	3450	0.67 at 200	-55 to +175	0.19	TO-244	Two diodes common cathode	Not Isolated
VS-402CNQ200PbF		551	175		2100	0.845 at 200	-40 to +175	0.19	TO-244	Two diodes common cathode	Not Isolated
VS-403CNQ100PbF		400	141	100	3300	0.84 at 200	-55 to +175	0.19	TO-244	Two diodes common cathode	Not Isolated
VS-409CNQ...PbF Series		400	129	135	2300	1.13 at 200	-55 to +175	0.19	TO-244	Two diodes common cathode	Not Isolated

Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-409CNQ...PbF Series		400	129	150	2300	1.13 at 200	-55 to +175	0.19	TO-244	Two diodes common cathode	Not Isolated
VS-440CNQ030PbF		440	125	30	3000	0.51 at 220	-55 to +150	0.19	TO-244	Two diodes common cathode	Not Isolated
VS-QA250FA20		250	106	200	900	1.0 at 125	-55 to +175	0.44	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-QA300FA17		300	132	170	1575	0.79 at 100	-55 to +175	0.26	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-VSKCS200/045		200	91	45	1700	0.67 at 100	-55 to +150	0.52	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKCS201/045		200	123	45	1850	0.72 at 100	-55 to +175	0.52	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKCS203/100		200	121	100	1700	0.99 at 100	-55 to +175	0.52	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKCS209/150		200	113	150	1600	1.01 at 100	-55 to +175	0.52	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKCS220/030		220	110	30	2000	0.59 at 110	-55 to +150	0.52	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKCS400/045		400	91	45	3400	0.67 at 200	-55 to +150	0.26	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKCS401/045		400	120	45	3400	0.72 at 200	-55 to +175	0.26	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated

Series	Product Image	$I_{FAV}$ (A)	$T_C$ (°C)	$V_{RRM}$ (V)	$I_{FSM}$ (A)	$V_F$ at $I_{FM}$ at 25 °C (V at A)	$T_J$ (°C)	$R_{thJC}$ (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSKCS403/100		400	104	100	2600	0.99 at 200	-55 to +175	0.32	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKCS408/060		400	102	60	3300	0.74 at 200	-55 to +150	0.26	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKCS409/150		400	105	150	2300	1.03 at 200	-55 to +175	0.32	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKCS440/030		440	97	30	3000	0.68 at 220	-55 to +150	0.26	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKDS200/045		100	91	45	1700	0.67 at 100	-55 to +150	0.52	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKDS201/045		100	123	45	1850	0.72 at 100	-55 to +175	0.52	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKDS203/100		100	121	100	1700	0.99 at 100	-55 to +175	0.52	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKDS209/150		100	113	150	1600	1.01 at 100	-55 to +175	0.52	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKDS220/030		110	110	30	2000	0.59 at 110	-55 to +150	0.52	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKDS400/045		200	91	45	3400	0.67 at 200	-55 to +150	0.26	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated

Series	Product Image	$I_{FAV}$ (A)	$T_C$ (°C)	$V_{RRM}$ (V)	$I_{FSM}$ (A)	$V_F$ at $I_{FM}$ at 25 °C (V at A)	$T_J$ (°C)	$R_{thJC}$ (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSKDS401/045		200	120	45	3450	0.72 at 200	-55 to +175	0.26	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKDS403/100		200	104	100	2600	0.99 at 200	-55 to +175	0.32	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKDS408/060		400	102	60	3300	0.74 at 200	-55 to +150	0.26	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKDS409/150		200	105	150	2300	1.03 at 200	-55 to +175	0.32	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKDS440/030		220	97	30	3000	0.68 at 220	-55 to +150	0.26	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKJS203/100		200	121	100	1700	0.99 at 100	-55 to +175	0.52	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKJS209/150		200	113	150	1600	1.01 at 100	-55 to +175	0.52	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKJS403/100		400	121	100	2600	0.99 at 200	-55 to +175	0.32	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKJS409/150		400	105	150	2300	1.03 at 200	-55 to +175	0.32	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKJS440/030		440	97	30	3000	0.68 at 220	-55 to +150	0.26	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated



# VS-T40HFL, VS-T70HFL, VS-T85HFL SERIES PRODUCT INFORMATION

Fast Recovery Diodes (T-Modules), 40 A, 70 A, 85 A

## FEATURES



- Fast recovery time characteristics
- Electrically isolated base plate
- 3500 VRMS isolating voltage











D-55 (T-module)


Circuit Configuration	$V_{DRM} / V_{RRM}$ (V) Range	$I_{FAV}$ (A)	$T_C$ (°C)	$I_{FSM}$ at 50 Hz (A)	$I_{FSM}$ at 60 Hz (A)	$V_{FM}$ at 25 °C Range (V)	$T_J$ (°C)
Single	100 - 1000	40	70	475	500	1.60	-40 to +125
Single	100 - 1000	70	70	830	870	1.73	-40 to +125
Single	100 - 1000	85	70	1300	1370	1.55	-40 to +125










## MODULES - MODULES, DIODE - HIGH VOLTAGE

Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-RA160FA120		91	90	1200	940	985	1.27 at 100	-55 to +150	0.26	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-RA220FA120		108	90	1200	1170	1225	1.31 at 110	-55 to +150	0.2	SOT-227	Two separate diodes, parallel pin-out	Isolated
VS-T20HF220		20	85	2200	450	470	1.50 at 60	-40 to +150	2.53	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		110	85	1200	2000	2100	1.35	-40 to +150	0.47	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		70	85	1200	1200	1250	1.35	-40 to +150	0.69	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		40	85	100	570	600	1.30	-40 to +150	1.36	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		40	85	1200	570	600	1.30	-40 to +150	1.36	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		85	85	1200	1700	1800	1.27	-40 to +150	0.62	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		110	85	1000	2000	2100	1.35	-40 to +150	0.47	D-55 (T-module)	Single diode	Isolated

Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		70	85	1000	1200	1250	1.35	-40 to +150	0.69	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		40	85	1000	570	600	1.30	-40 to +150	1.36	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		85	85	1000	1700	1800	1.27	-40 to +150	0.62	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		110	85	800	2000	2100	1.35	-40 to +150	0.47	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		70	85	800	1200	1250	1.35	-40 to +150	0.69	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		40	85	800	570	600	1.30	-40 to +150	1.36	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		85	85	800	1700	1800	1.27	-40 to +150	0.62	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		110	85	600	2000	2100	1.35	-40 to +150	0.47	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		70	85	600	1200	1250	1.35	-40 to +150	0.69	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		40	85	600	570	600	1.30	-40 to +150	1.36	D-55 (T-module)	Single diode	Isolated

Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		85	85	600	1700	1800	1.27	-40 to +150	0.62	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		70	85	400	1200	1250	1.35	-40 to +150	0.69	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		110	85	400	2000	2100	1.35	-40 to +150	0.47	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		40	85	400	570	600	1.30	-40 to +150	1.36	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		85	85	400	1700	1800	1.27	-40 to +150	0.62	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		70	85	200	1200	1250	1.35	-40 to +150	0.69	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		110	85	200	2000	2100	1.35	-40 to +150	0.47	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		85	85	100	1700	1800	1.27	-40 to +150	0.62	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		40	85	200	570	600	1.30	-40 to +150	1.36	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		85	85	200	1700	1800	1.27	-40 to +150	0.62	D-55 (T-module)	Single diode	Isolated

Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		70	85	100	1200	1250	1.35	-40 to +150	0.69	D-55 (T-module)	Single diode	Isolated
VS-T40HF..., VS-T70HF..., VS-T85HF..., VS-T110HF... Series		110	85	100	2000	2100	1.35	-40 to +150	0.47	D-55 (T-module)	Single diode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	800	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes common anode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	1400	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	400	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	1200	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Single diode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	1600	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	1600	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes common anode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	800	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	1400	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Single diode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	400	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes common anode	Isolated


Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	1200	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	1600	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Single diode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	1600	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	800	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Single diode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	1400	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	1400	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes common anode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	400	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	1200	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Single diode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	1600	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes common anode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	800	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	1400	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Single diode	Isolated












Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	1400	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	400	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Single diode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	1200	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	1200	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes common anode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	1600	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	800	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Single diode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	1400	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes common anode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	1400	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	400	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	1200	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Single diode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	1200	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes doubler circuit	Isolated












Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	1600	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes common anode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	800	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes common anode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	800	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	1400	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	400	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Single diode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	1200	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes common anode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	1200	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	1600	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	800	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Single diode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	800	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	1400	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes common anode	Isolated























Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	400	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes common anode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	400	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	1200	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	1600	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Single diode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	800	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	800	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes common anode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	1400	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	400	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Single diode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	400	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	1200	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes common anode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	1600	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes common cathode	Isolated

Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	1600	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	800	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		165	100	1400	4000	4200	1.43	-40 to +150	0.2	INT-A-PAK	Single diode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	400	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes common cathode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	400	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Two diodes common anode	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		230	100	1200	5500	5765	1.46	-40 to +150	0.14	INT-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.166..PbF, VS-VSK.196..PbF, VS-VSK.236..PbF Series		195	100	1600	4750	4980	1.38	-40 to +150	0.16	INT-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	800	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	400	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	400	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	3000	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Two diodes doubler circuit	Isolated

Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	400	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	400	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	3000	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	3000	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	400	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	3000	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	3000	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	2000	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	3000	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	3000	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	2000	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Two diodes doubler circuit	Isolated


Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	2000	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	3000	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	3000	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	2000	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	2000	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	1600	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	2000	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	2000	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	1600	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	1600	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	2000	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Two diodes common cathode	Isolated

Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	2000	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	1600	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	1600	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	1200	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	1600	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	1600	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	1200	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	1600	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	1600	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	1200	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	1200	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Two diodes doubler circuit	Isolated












Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	800	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	1200	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	1200	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	800	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	1200	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	1200	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	800	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	800	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	400	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	1200	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	800	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Two diodes common cathode	Isolated






Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	800	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	400	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		270	100	800	8920	9430	1.48	-40 to +150	0.125	MAGN-A-PAK	Two diodes common cathode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	800	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Single diode	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		320	100	400	10110	10580	1.28	-40 to +150	0.125	MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSK.250PbF, VS-VSK.270PbF, VS-VSK.320PbF Series		250	100	400	7015	7345	1.29	-40 to +150	0.16	MAGN-A-PAK	Single diode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1600	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	400	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1000	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1600	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1000	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1400	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Single diode	Isolated
















Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1600	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	800	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1400	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	800	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1200	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1400	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	600	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1200	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	600	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1000	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1200	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	400	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated























Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1000	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	400	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	800	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1600	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	800	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	600	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1400	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	600	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	400	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD56..., VS-VSKE56..., VS-VSKJ56..., VS-VSKC56.. Series		60	114	1200	1300	1360	1.6	-40 to +150	0.33	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD600 Series		600	100	2000	19000	20100	1.45 at 1800	-40 to +150	0.065	Super MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSKD600 Series		600	100	800	19000	20100	1.45 at 1800	-40 to +150	0.065	Super MAGN-A-PAK	Two diodes doubler circuit	Isolated

Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSKD600 Series		600	100	1200	19000	20100	1.45 at 1800	-40 to +150	0.065	Super MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSKD600 Series		600	100	1600	19000	20100	1.45 at 1800	-40 to +150	0.065	Super MAGN-A-PAK	Two diodes doubler circuit	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1200	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1400	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1200	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1200	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1000	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1200	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1000	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1000	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	800	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1000	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated

Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	600	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	800	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	800	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	600	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	800	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	400	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	600	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	400	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	600	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1600	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	400	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1600	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated

Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1600	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	400	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1400	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1600	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1400	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD71..., VS-VSKE71..., VS-VSKJ71..., VS-VSKC71.. Series		80	110	1400	1500	1570	1.6	-40 to +150	0.28	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1400	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1600	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1200	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1400	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1200	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1400	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated









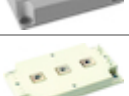

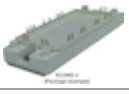

Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1000	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1200	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1000	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1200	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	800	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1000	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	800	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1000	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	600	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	800	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	600	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	800	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated

Series	Product Image	I <sub>FAV</sub> (A)	T <sub>C</sub> (°C)	V <sub>RRM</sub> (V)	I <sub>FSM</sub> at 50 Hz (A)	I <sub>FSM</sub> at 60 Hz (A)	V <sub>F</sub> at I <sub>FM</sub> at 25 °C (V at A)	T <sub>J</sub> (°C)	R <sub>thJC</sub> (°C/W)	Package	Circuit Configuration	Isolation from Heatsink
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	400	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	600	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1600	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	400	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	600	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1600	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes common anode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	400	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1400	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Single diode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	1600	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes common cathode	Isolated
VS-VSKD91..., VS-VSKC91..., VS-VSKJ91..., VS-VSKE91.. Series		100	112	400	2020	2115	1.55	-40 to +150	0.22	AAP Gen 7 (TO-240AA)	Two diodes doubler circuit	Isolated
VS-VSMD400AW60, VS-VSMD400CW60		400	133	600	2500	2620	1.31 at 200	-40 to +175	0.1	TO-244	Two diodes common anode	Not Isolated
VS-VSMD400AW60, VS-VSMD400CW60		400	133	600	2500	2620	1.31 at 200	-40 to +175	0.1	TO-244	Two diodes common cathode	Not Isolated





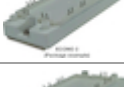
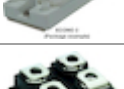





## MODULES - MODULES, IGBT

Series	Product Image	I <sub>c</sub> (A)	T <sub>C</sub> (°C)	V <sub>CE(S)</sub> (V)	V <sub>CE(on) Typ.</sub> at 25 °C (V)	E <sub>on Typ.</sub> at 125 °C (mJ)	E <sub>off Typ.</sub> at 125 °C (mJ)	T <sub>J</sub> (°C)	Package	Circuit Configuration
VS-20MT120PFP		42	80	1200	1.84	1.08	1.18	150	MTP	Full bridge
VS-40MT120PHAPbF		40	102	1200	2.24	1.02	1.83	150	MTP	Half bridge
VS-50MT060PHTAPbF		50	117	600	1.41	1.46	0.62	150	MTP	Half bridge
VS-ETF075Y60U		113	80	600	1.7	1	1.83	175	EMIPAK 2B	3-levels half bridge inverter stage
VS-ETF075Y60U		80	80	600	1.7	1	1.83	175	EMIPAK 2B	3-levels half bridge inverter stage
VS-GP100TS60SFPbF		235	80	600	1.16	2	15.3	150	INT-A-PAK	Half bridge
VS-GP250SA60S		239	90	600	1.1	2.6	21.5	150	SOT-227	Single switch no diode
VS-GP300TD60S		400	80	600	1.3	7.2	55.2	150	Dual INT-A-PAK low profile	Half bridge
VS-GP400TD60S		525	80	600	1.3	7.2	74	150	Dual INT-A-PAK low profile	Half bridge










Series	Product Image	I <sub>c</sub> (A)	T <sub>c</sub> (°C)	V <sub>CES</sub> (V)	V <sub>CE(on)</sub> Typ. at 25 °C (V)	E <sub>on</sub> Typ. at 125 °C (mJ)	E <sub>off</sub> Typ. at 125 °C (mJ)	T <sub>J</sub> (°C)	Package	Circuit Configuration
VS-GT100DA120UF		123	90	1200	1.93	3.9	7.1	150	SOT-227	Single switch with AP diode
VS-GT100TP60N		100	80	600	1.65		1.73	175	INT-A-PAK	Half bridge
VS-GT140DA60U		140	90	600	1.72	0.43	2.12	175	SOT-227	Single switch with AP diode
VS-GT180DA120U		185	90	1200	1.55	5.7	11.6	150	SOT-227	Single switch with AP diode
VS-GT200TP065U		132	80	650	1.9	2.82	3.86	175	INT-A-PAK	Half bridge
VS-GT250SA60S		250	90	600	1.16	2.03	9.65	175	SOT-227	Single switch no diode
VS-GT300FD060N		288	80	600	1.72	10.7	15.6	175	Dual INT-A-PAK low profile	3-level half bridge inverter stage
VS-GT300TD60S		349	80	600	1.15	2	20	175	Dual INT-A-PAK low profile	Half bridge
VS-GT300YH120N		400	25	1200	1.93				Dual INT-A-PAK	Current fed inverter topology
VS-GT400TD60S		532	80	600	1.14	2.2	27.6	175	Dual INT-A-PAK low profile	Half bridge
VS-GT400TH60N		400	80	600	1.6	23.2	16.8	175	Dual INT-A-PAK	Half bridge
VS-GT50YF120NT		44	80	1200	2.34	1.58	2.52	150	ECONO 2	4 pack
























Series	Product Image	I <sub>c</sub> (A)	T <sub>c</sub> (°C)	V <sub>CES</sub> (V)	V <sub>CE(on)</sub> Typ. at 25 °C (V)	E <sub>on</sub> Typ. at 125 °C (mJ)	E <sub>off</sub> Typ. at 125 °C (mJ)	T <sub>J</sub> (°C)	Package	Circuit Configuration
VS-GT55LA120UX		47	80	1200	2.39	4.1	2.3	150	SOT-227	Low side chopper
VS-GT55NA120UX		47	80	1200	2.39	4.1	2.3	150	SOT-227	High side chopper
VS-GT75LA60UF		61	80	600	1.79	0.95	0.53	175	SOT-227	Low side chopper
VS-GT75NA60UF		61	80	600	1.79	0.95	0.53	175	SOT-227	High side chopper
VS-GT75YF120NT		81	80	1200	2.2	3.35	4.28	150	ECONO 2	4 pack
VS-GT75YF120UT		81	80	1200	2.2	3.17	4.23	150	ECONO 2	4 pack with thermistor
VS-GT80DA120U		93	90	1200	2	3.9	5.5	150	SOT-227	Single switch with AP diode
VS-GT80DA60U		85	90	600	1.83	2.3	1.43	175	SOT-227	Single switch with AP diode
VS-GT90DA120U		106	90	1200	2.17	2.23	3.87	150	SOT-227	Single switch with AP diode
VS-GT90DA60U		92	90	600	1.64	1.81	1	150	SOT-227	Single switch with AP diode
VS-GT90SA120U		106	90	1200	2.17	2.23	3.87	150	SOT-227	Single switch no diode










## MODULES - MODULES, MOSFET












Series	Product Image	Package	Circuit Configuration	V <sub>DSS</sub> (V)	I <sub>D</sub> (A)	T <sub>C</sub> (°C)	R <sub>DS(on)</sub> (mΩ)	T <sub>J</sub> (°C)	E <sub>AS</sub> (mJ)	I <sub>AR</sub> (A)
VS-ETY020P120F		EMIPAK 2B	Full bridge inverter	1200	26	80	71.0	175		
VS-FA40SA50LC		SOT-227	Single switch	500	29	90	106.0	-55 to +150	400	13
VS-FA72SA50LC		SOT-227	Single switch	500	52	90	61.5	-55 to +150	725	22
VS-FC270SA20		SOT-227	Single switch	200	219	90	3.3	-55 to +175	650	180
VS-FC420SA10		SOT-227	Single switch	100	330	90	1.3	-55 to +175	11 500	48
VS-FC420SA15		SOT-227	Single switch	150	300	90	1.93	-55 to +175	720	120
VS-FC80NA20		SOT-227	High side chopper	200	83	90	9.6	-55 to +175	600	50








# MODULES - MODULES, THYRISTOR










Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-P100 Series		25	85	25	400	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge common cathode
VS-P100 Series		25	85	25	400	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge doubler connection
VS-P100 Series		25	85	25	400	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, all SCR bridge
VS-P100 Series		25	85	25	1000	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge common cathode
VS-P100 Series		25	85	25	1000	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge doubler connection
VS-P100 Series		25	85	25	1000	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, all SCR bridge
VS-P100 Series		25	85	25	600	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge common cathode
VS-P100 Series		25	85	25	600	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge doubler connection
VS-P100 Series		25	85	25	600	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, all SCR bridge
VS-P100 Series		25	85	25	1200	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge common cathode

Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-P100 Series		25	85	25	1200	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge doubler connection
VS-P100 Series		25	85	25	1200	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, all SCR bridge
VS-P100 Series		25	85	25	800	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge common cathode
VS-P100 Series		25	85	25	800	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge doubler connection
VS-P100 Series		25	85	25	800	357	375	1.35	-40 to +125	200	PACE-PAK (D-19)	Single phase, all SCR bridge
VS-P400 Series		40	80	40	800	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge doubler connection
VS-P400 Series		40	80	40	800	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, all SCR bridge
VS-P400 Series		40	80	40	400	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge common cathode
VS-P400 Series		40	80	40	400	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge doubler connection
VS-P400 Series		40	80	40	400	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, all SCR bridge
VS-P400 Series		40	80	40	1000	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge common cathode
VS-P400 Series		40	80	40	1000	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge doubler connection












Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-P400 Series		40	80	40	1000	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, all SCR bridge
VS-P400 Series		40	80	40	600	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge common cathode
VS-P400 Series		40	80	40	600	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge doubler connection
VS-P400 Series		40	80	40	600	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, all SCR bridge
VS-P400 Series		40	80	40	1200	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge common cathode
VS-P400 Series		40	80	40	1200	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge doubler connection
VS-P400 Series		40	80	40	1200	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, all SCR bridge
VS-P400 Series		40	80	40	800	385	400	1.4	-40 to +125	200	PACE-PAK (D-19)	Single phase, hybrid bridge common cathode
VS-T..RIA Series		90	70	141	100	1780	1870	1.55	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		70	70	110	400	1660	1740	1.55	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		50	70	80	800	1310	1370	1.60	-40 to +125	500	D-55 (T-module)	Single SCR












Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-T..RIA Series		90	70	141	1000	1780	1870	1.55	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		50	70	80	100	1310	1370	1.60	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		90	70	141	200	1780	1870	1.55	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		70	70	110	600	1660	1740	1.55	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		50	70	80	1000	1310	1370	1.60	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		90	70	141	1200	1780	1870	1.55	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		50	70	80	200	1310	1370	1.60	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		90	70	141	400	1780	1870	1.55	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		70	70	110	800	1660	1740	1.55	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		50	70	80	1200	1310	1370	1.60	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		70	70	110	100	1660	1740	1.55	-40 to +125	500	D-55 (T-module)	Single SCR


Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-T..RIA Series		50	70	80	400	1310	1370	1.60	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		90	70	141	600	1780	1870	1.55	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		70	70	110	1000	1660	1740	1.55	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		70	70	110	200	1660	1740	1.55	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		50	70	80	600	1310	1370	1.60	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		90	70	141	800	1780	1870	1.55	-40 to +125	500	D-55 (T-module)	Single SCR
VS-T..RIA Series		70	70	110	1200	1660	1740	1.55	-40 to +125	500	D-55 (T-module)	Single SCR
VS-TA160SA120		158	75	0	1200	1390	1455	1.45	-40 to +125	1000	SOT-227	Single thyristor
VS-TA60DA160		60	110	0	1600	850	890	1.50	-40 to +150	1000	SOT-227	Two thyristors, back to back
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	1600	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	1600	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	1200	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit













Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	1600	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	1600	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	1200	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	1200	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	1600	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	1200	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	1200	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	800	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	1200	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	1200	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	1200	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	400	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit



























Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	400	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	1200	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	400	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	400	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	400	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	400	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	1400	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	400	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	400	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	1400	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	1400	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	1400	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control













Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	1400	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	1400	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	1400	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	1400	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	800	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	800	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	1400	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	800	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	800	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	400	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	800	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	800	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control

Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	1600	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	800	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	800	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		140	85	310	1600	4500	4712	1.55	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		160	85	355	1600	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit
VS-VSK.136..PbF, VS-VSK.142..PbF, VS-VSK.162..PbF Series		135	85	300	1600	3200	3360	1.57	-40 to +125	1000	INT-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1600	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1400	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	800	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1000	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1600	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1000	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control













Series	Product Image	$I_{T(AV)}$ (A)	$T_C$ (°C)	$I_{T(RMS)}$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	400	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	800	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1600	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1800	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1200	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1000	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	2000	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	400	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1200	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	400	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1400	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1600	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit

Series	Product Image	$I_{T(AV)}$ (A)	$T_C$ (°C)	$I_{T(RMS)}$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1600	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1200	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1400	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	800	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	400	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1600	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	800	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1800	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1000	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1200	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1200	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	800	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes













Series	Product Image	$I_{T(AV)}$ (A)	$T_C$ (°C)	$I_{T(RMS)}$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1000	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1800	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	2000	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1400	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1200	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	400	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	800	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1400	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	800	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1600	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	400	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	400	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control













Series	Product Image	$I_{T(AV)}$ (A)	$T_C$ (°C)	$I_{T(RMS)}$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1400	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1600	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1000	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	800	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1800	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1000	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	2000	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1200	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1400	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	1400	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1000	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	1200	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control



























Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSK.170PbF, VS-VSK.250PbF Series		250	85	555	2000	8500	8900	1.44	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.170PbF, VS-VSK.250PbF Series		170	85	377	400	5100	5350	1.60	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.230..PbF Series		230	85	510	1600	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.230..PbF Series		230	85	510	1600	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.230..PbF Series		230	85	510	800	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.230..PbF Series		230	85	510	800	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.230..PbF Series		230	85	510	800	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.230..PbF Series		230	85	510	800	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.230..PbF Series		230	85	510	1800	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.230..PbF Series		230	85	510	1800	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.230..PbF Series		230	85	510	1800	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.230..PbF Series		230	85	510	1800	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes























Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSK.230..PbF Series		230	85	510	1200	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.230..PbF Series		230	85	510	1200	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.230..PbF Series		230	85	510	1200	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.230..PbF Series		230	85	510	1200	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.230..PbF Series		230	85	510	2000	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.230..PbF Series		230	85	510	2000	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.230..PbF Series		230	85	510	2000	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSK.230..PbF Series		230	85	510	2000	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	Two SCRs common cathodes
VS-VSK.230..PbF Series		230	85	510	1600	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSK.230..PbF Series		230	85	510	1600	7500	7850	1.59	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1400	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1000	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control













Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	600	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1600	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	400	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1200	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1200	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1400	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	800	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	800	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1000	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	600	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1600	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1200	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control

Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1400	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	800	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	800	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1000	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	400	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	400	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	600	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1400	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1600	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1200	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	800	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1000	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit













Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	400	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	400	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	600	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1400	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1400	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1600	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1000	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1200	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	800	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	400	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1400	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1600	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit

Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1000	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1000	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1200	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	600	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	600	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	800	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1600	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	400	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1400	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1000	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1200	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	600	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes













Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	600	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	800	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1600	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSK.41..., VS-VSK.56.. Series		45	85	100	1600	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	400	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSK.41..., VS-VSK.56.. Series		60	85	135	1200	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKH320-16PbF Series		320	70	502	1600	9000	9420	1.50	-40 to +130	1000	MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSKH570-16PbF		570	85	894	1600	18000	18800	1.36	-40 to +135	1000	Super MAGN-A-PAK	SCR/diode doubler circuit
VS-VSKH570-18PbF		570	74	895	1800	17800	18700	1.50	-40 to +135	1000	Super MAGN-A-PAK	SCR/diode doubler circuit
VS-VSKL300/08PbF		300	53	116	800	6500	6900	1.35	-40 to +140	500	INT-A-PAK	SCR/diode doubler circuit, negative control
VS-VSKS500/08PbF		500	76	785	800	14000	14658	1.1	-40 to +130	500	MAGN-A-PAK block	Single SCR
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1000	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit













Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	600	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1600	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1200	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	800	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	400	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1600	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1200	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	800	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	400	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1400	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1200	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	800	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control



























Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	400	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1400	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1000	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	600	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	800	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	400	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1400	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1000	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	600	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1600	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1400	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1000	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control














Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	600	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1600	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT105..., VS-VSKH105..., VS-VSKL105..., VS-VSKN105.. Series		105	85	235	1200	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT152/04PbF		150	85	330	400	4000	4200	1.48	-40 to +125	1000	INT-A-PAK	Two SCRs doubler circuit
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1400	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	600	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1600	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1200	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	800	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	400	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	400	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1400	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes













Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1000	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1200	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	800	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	400	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1400	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1400	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1000	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1000	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	600	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1600	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	800	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	400	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control

Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1400	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1000	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	600	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	600	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1600	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1200	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	400	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1400	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1000	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	600	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1600	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1600	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes













Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1200	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1200	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	800	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1000	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	600	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1600	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	1200	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	800	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	800	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKT26..., VS-VSKH26..., VS-VSKL26..., VS-VSKN26.. Series		27	85	60	400	400	420	1.65	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKT320PbF Series		320	70	710	1600	9000	9420	1.40	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit
VS-VSKT320PbF Series		320	70	710	1200	9000	9420	1.40	-40 to +130	1000	MAGN-A-PAK	Two SCRs doubler circuit

Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSKT500-..PbF, VS-VSKH500-..PbF, VS-VSKL500-..PbF Series		500	82	785	1200	17.8	18.7	1.50	-40 to +130	1000	Super MAGN-A-PAK	Two SCRs doubler circuit
VS-VSKT500-..PbF, VS-VSKH500-..PbF, VS-VSKL500-..PbF Series		500	82	785	1400	17.8	18.7	1.50	-40 to +130	1000	Super MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSKT500-..PbF, VS-VSKH500-..PbF, VS-VSKL500-..PbF Series		500	82	785	1600	17.8	18.7	1.50	-40 to +130	1000	Super MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSKT500-..PbF, VS-VSKH500-..PbF, VS-VSKL500-..PbF Series		500	82	785	800	17.8	18.7	1.50	-40 to +130	1000	Super MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSKT500-..PbF, VS-VSKH500-..PbF, VS-VSKL500-..PbF Series		500	82	785	1200	17.8	18.7	1.50	-40 to +130	1000	Super MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSKT500-..PbF, VS-VSKH500-..PbF, VS-VSKL500-..PbF Series		500	82	785	1400	17.8	18.7	1.50	-40 to +130	1000	Super MAGN-A-PAK	Two SCRs doubler circuit
VS-VSKT500-..PbF, VS-VSKH500-..PbF, VS-VSKL500-..PbF Series		500	82	785	1600	17.8	18.7	1.50	-40 to +130	1000	Super MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSKT500-..PbF, VS-VSKH500-..PbF, VS-VSKL500-..PbF Series		500	82	785	800	17.8	18.7	1.50	-40 to +130	1000	Super MAGN-A-PAK	Two SCRs doubler circuit
VS-VSKT500-..PbF, VS-VSKH500-..PbF, VS-VSKL500-..PbF Series		500	82	785	1200	17.8	18.7	1.50	-40 to +130	1000	Super MAGN-A-PAK	SCR/diode doubler circuit, positive control
VS-VSKT500-..PbF, VS-VSKH500-..PbF, VS-VSKL500-..PbF Series		500	82	785	1400	17.8	18.7	1.50	-40 to +130	1000	Super MAGN-A-PAK	SCR/diode doubler circuit, negative control













Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-VSKT500-..PbF, VS-VSKH500-..PbF, VS-VSKL500-..PbF Series		500	82	785	1600	17.8	18.7	1.50	-40 to +130	1000	Super MAGN-A-PAK	Two SCRs doubler circuit
VS-VSKT500-..PbF, VS-VSKH500-..PbF, VS-VSKL500-..PbF Series		500	82	785	800	17.8	18.7	1.50	-40 to +130	1000	Super MAGN-A-PAK	SCR/diode doubler circuit, negative control
VS-VSKT570-16PbF		570	85	894	1600	18000	18800	1.36	-40 to +135	1000	Super MAGN-A-PAK	Two SCRs doubler circuit
VS-VSKT570-18PbF		570	74	895	1800	17800	18700	1.50	-40 to +135	1000	Super MAGN-A-PAK	Two SCRs doubler circuit
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	400	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1400	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1000	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	800	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	400	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1400	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1000	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	600	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes













Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	400	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1600	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1400	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1000	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	600	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1600	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1200	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1000	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	600	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1600	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1200	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	800	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes










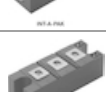
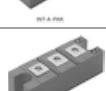















Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	600	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1600	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1200	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	800	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	400	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1400	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	1200	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT71..., VS-VSKH71..., VS-VSKL71..., VS-VSKN71.. Series		75	85	165	800	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1600	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1200	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	800	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	600	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit























Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1600	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1200	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	800	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	400	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1400	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1200	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	800	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	400	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1400	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1000	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	800	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	400	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control

Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1400	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1000	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	600	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1600	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	400	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1400	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1000	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	600	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1600	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, negative control
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1200	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode common anodes
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	1000	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs doubler circuit
VS-VSKT91..., VS-VSKH91..., VS-VSKL91..., VS-VSKN91.. Series		95	85	210	600	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	SCR/diode doubler circuit, positive control

Series	Product Image	$I_{T(AV)}$ (A)	$T_C$ (°C)	$I_{T(RMS)}$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSKU105..., VS-VSKV105.. Series		105	85	165	400	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU105..., VS-VSKV105.. Series		105	85	165	800	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU105..., VS-VSKV105.. Series		105	85	165	1600	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU105..., VS-VSKV105.. Series		105	85	165	800	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU105..., VS-VSKV105.. Series		105	85	165	1200	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU105..., VS-VSKV105.. Series		105	85	165	400	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU105..., VS-VSKV105.. Series		105	85	165	1200	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU105..., VS-VSKV105.. Series		105	85	165	1600	2000	2094	1.8	-40 to +130	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU162...PbF, VS-VSKV162...PbF Series		160	85	355	1200	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	Two SCRs common anodes
VS-VSKU162...PbF, VS-VSKV162...PbF Series		160	85	355	1200	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	Two SCRs common cathodes
VS-VSKU162...PbF, VS-VSKV162...PbF Series		160	85	355	1600	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	Two SCRs common anodes
VS-VSKU162...PbF, VS-VSKV162...PbF Series		160	85	355	1600	4870	5100	1.54	-40 to +125	1000	INT-A-PAK	Two SCRs common cathodes

Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		60	85	95	800	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		60	85	95	400	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		45	85	70	1600	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		45	85	70	1200	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		60	85	95	1600	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		45	85	70	800	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		45	85	70	400	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		60	85	95	800	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		60	85	95	1200	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		60	85	95	400	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		45	85	70	1600	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		45	85	70	1200	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes

Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/μs)	Package	Circuit Configuration
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		45	85	70	800	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		60	85	95	1200	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		45	85	70	400	850	890	1.81	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU41..., VS-VSKV41..., VS-VSKU56..., VS-VSKV56.. Series		60	85	95	1600	1200	1256	1.7	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU71..., VS-VSKV71.. Series		75	85	115	800	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU71..., VS-VSKV71.. Series		75	85	115	1200	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU71..., VS-VSKV71.. Series		75	85	115	400	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU71..., VS-VSKV71.. Series		75	85	115	1200	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU71..., VS-VSKV71.. Series		75	85	115	1600	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU71..., VS-VSKV71.. Series		75	85	115	400	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU71..., VS-VSKV71.. Series		75	85	115	800	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU71..., VS-VSKV71.. Series		75	85	115	1600	1300	1360	1.72	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes

Series	Product Image	$I_T(AV)$ (A)	$T_C$ (°C)	$I_T(RMS)$ (A)	$V_{DRM}/V_{RRM}$ (V)	$I_{TSM}$ at 50 Hz (A)	$I_{TSM}$ at 60 Hz (A)	$V_{TM}$ at 25 °C (V)	$T_J$ (°C)	$dV/dt$ (V/ $\mu$ s)	Package	Circuit Configuration
VS-VSKU91..., VS-VSKV91.. Series		95	85	150	1600	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU91..., VS-VSKV91.. Series		95	85	150	800	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU91..., VS-VSKV91.. Series		95	85	150	1200	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU91..., VS-VSKV91.. Series		95	85	150	400	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU91..., VS-VSKV91.. Series		95	85	150	1200	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU91..., VS-VSKV91.. Series		95	85	150	1600	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes
VS-VSKU91..., VS-VSKV91.. Series		95	85	150	400	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common cathodes
VS-VSKU91..., VS-VSKV91.. Series		95	85	150	800	2000	2094	1.73	-40 to +125	1000	AAP Gen 7 (TO-240AA)	Two SCRs common anodes

**По вопросам продаж и поддержки обращайтесь:**

Алматы (7273)495-231  
Архангельск (8182)63-90-72  
Астрахань (8512)99-46-04  
Барнаул (3852)73-04-60  
Белгород (4722)40-23-64  
Брянск (4832)59-03-52  
Владивосток (423)249-28-31  
Волгоград (844)278-03-48  
Вологда (8172)26-41-59  
Воронеж (473)204-51-73  
Екатеринбург (343)384-55-89  
Иваново (4932)77-34-06  
Ижевск (3412)26-03-58  
Иркутск (395)279-98-46  
Россия (495)268-04-70

Казань (843)206-01-48  
Калининград (4012)72-03-81  
Калуга (4842)92-23-67  
Кемерово (3842)65-04-62  
Киров (8332)68-02-04  
Краснодар (861)203-40-90  
Красноярск (391)204-63-61  
Курск (4712)77-13-04  
Липецк (4742)52-20-81  
Магнитогорск (3519)55-03-13  
Москва (495)268-04-70  
Мурманск (8152)59-64-93  
Набережные Челны (8552)20-53-41  
Нижний Новгород (831)429-08-12  
Киргизия (996)312-96-26-47

Новокузнецк (3843)20-46-81  
Новосибирск (383)227-86-73  
Омск (3812)21-46-40  
Орел (4862)44-53-42  
Оренбург (3532)37-68-04  
Пенза (8412)22-31-16  
Пермь (342)205-81-47  
Ростов-на-Дону (863)308-18-15  
Рязань (4912)46-61-64  
Самара (846)206-03-16  
Санкт-Петербург (812)309-46-40  
Саратов (845)249-38-78  
Севастополь (8692)22-31-93  
Симферополь (3652)67-13-56  
Казахстан (7172)727-132

Смоленск (4812)29-41-54  
Сочи (862)225-72-31  
Ставрополь (8652)20-65-13  
Сургут (3462)77-98-35  
Тверь (4822)63-31-35  
Томск (3822)98-41-53  
Тула (4872)74-02-29  
Тюмень (3452)66-21-18  
Ульяновск (8422)24-23-59  
Уфа (347)229-48-12  
Хабаровск (4212)92-98-04  
Челябинск (351)202-03-61  
Череповец (8202)49-02-64  
Ярославль (4852)69-52-93